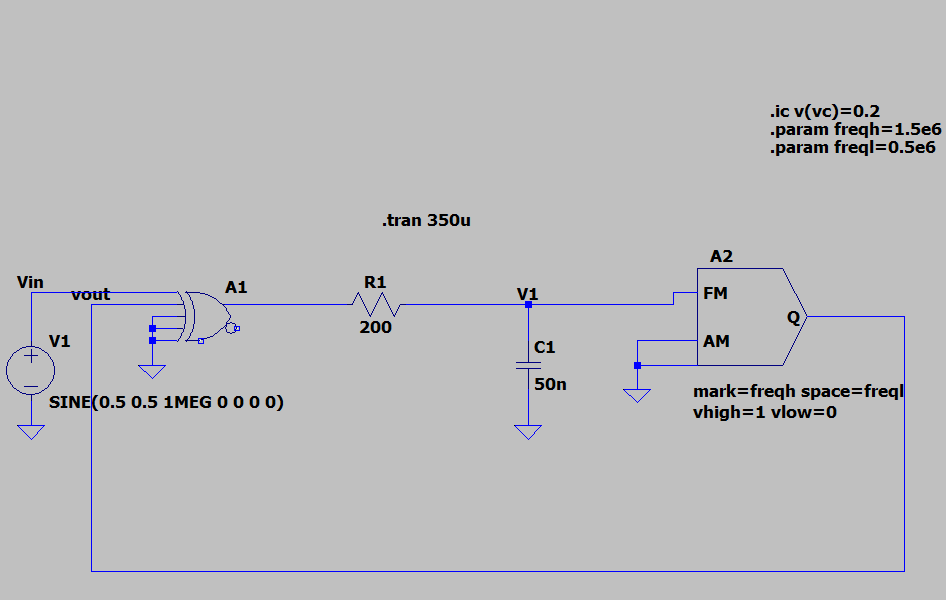
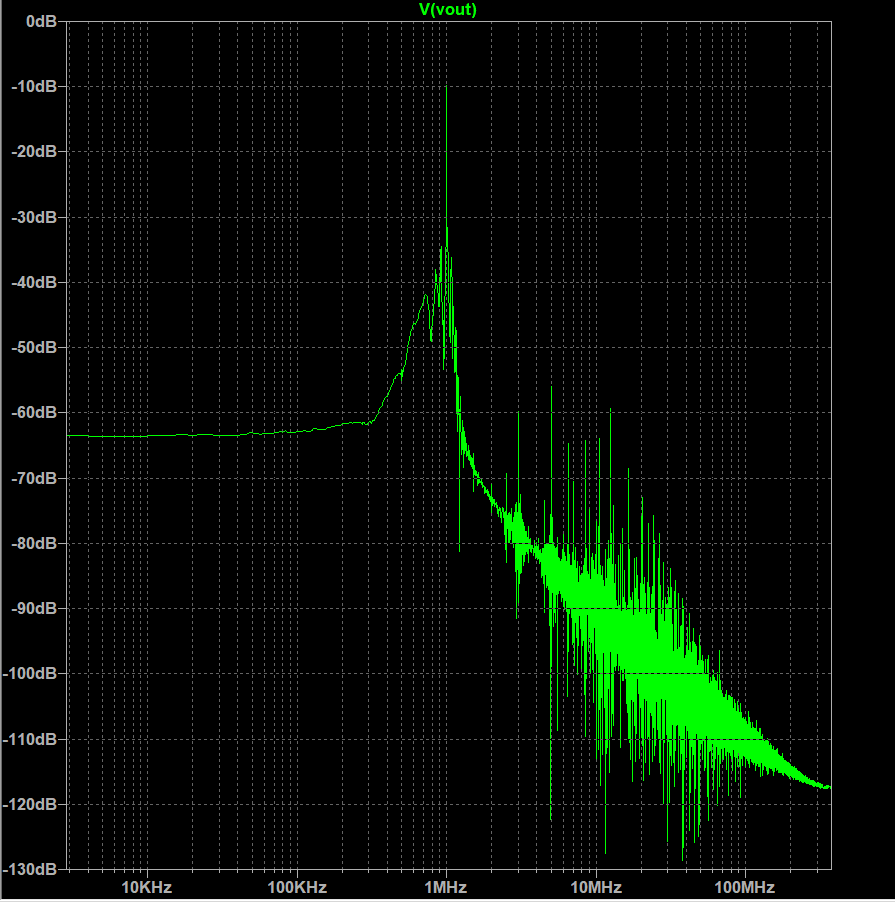
**SIMULATION OF Type-I PLL**

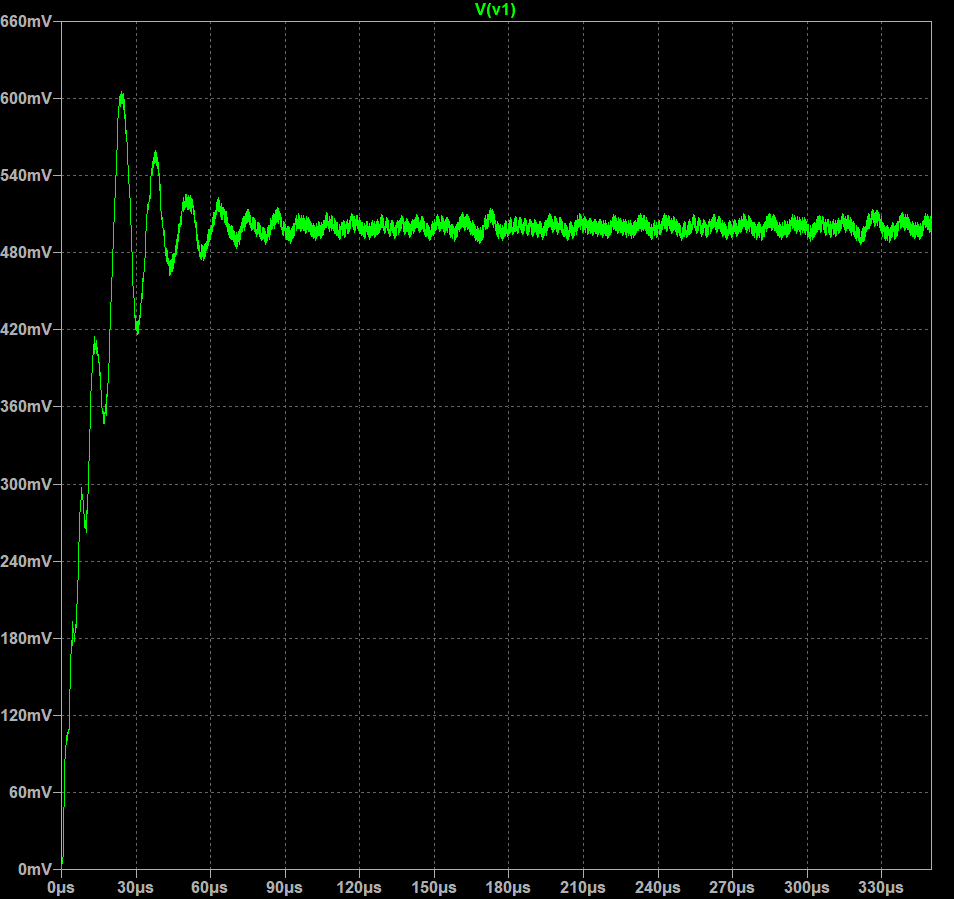
**Circuit Diagram:**

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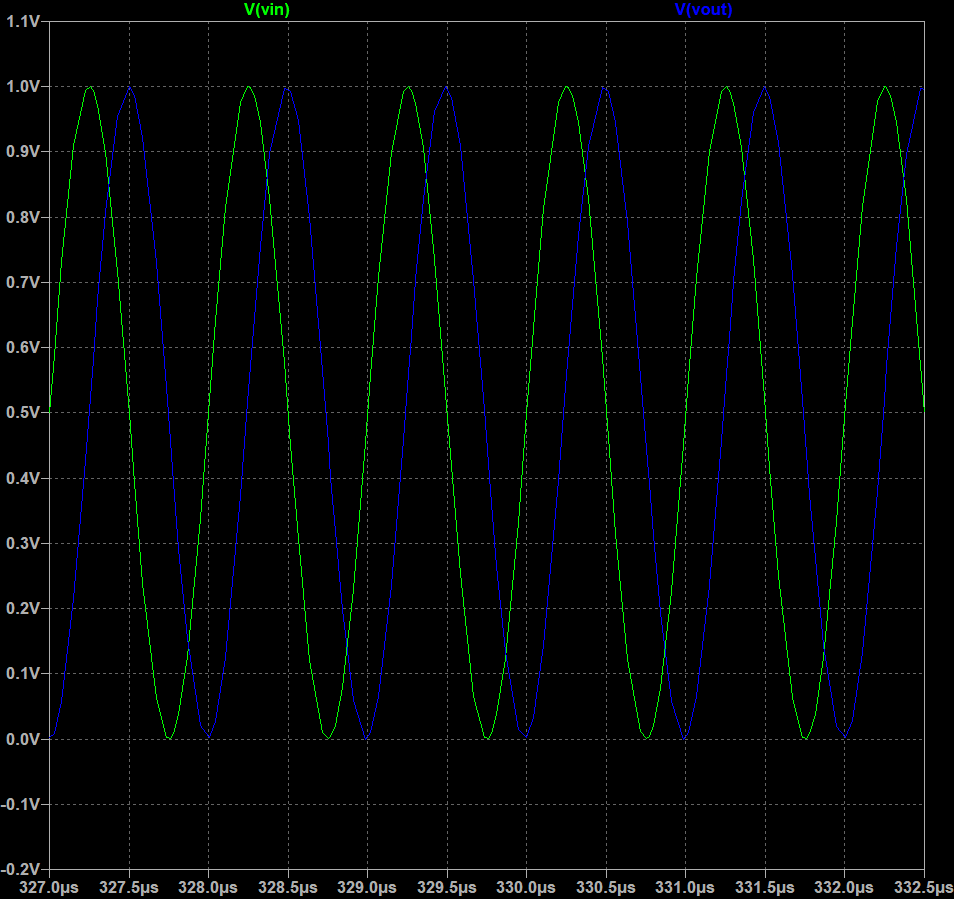
**Output: (FFT of Vout)**

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**Output of Vcont:**



**Comparison of Vin and Vout:**



**Validation of PLL locked:**

